Title: Network with Programmable Interconnect Nodes Adapted to Large Integrated Circuits Inventor: Klecka, et al

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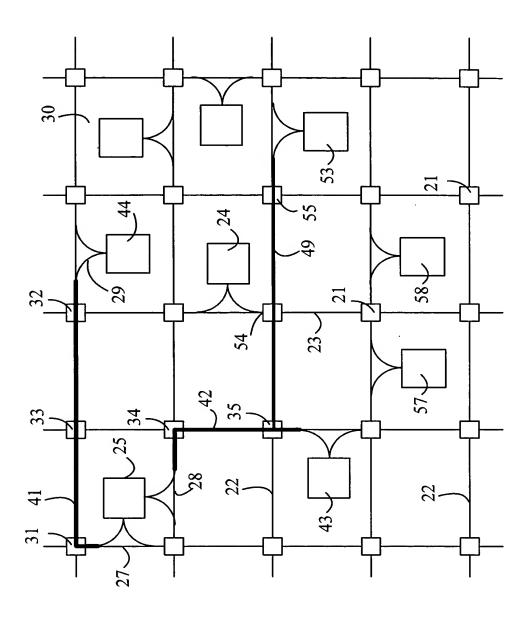
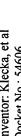


FIGURE 1

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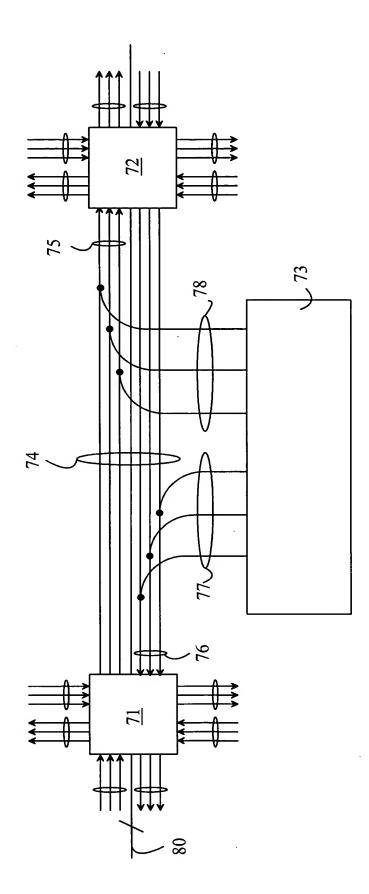
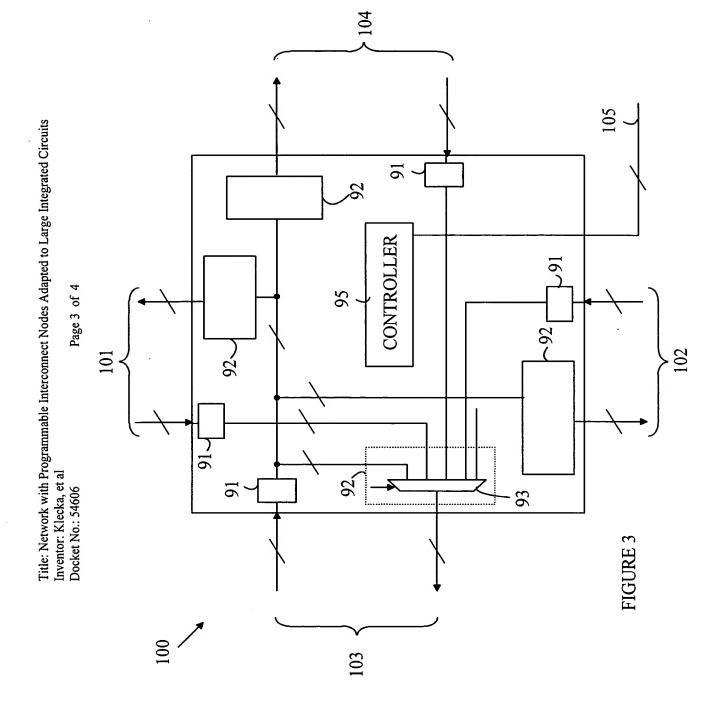


FIGURE 2



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FIGURE 4

